

Docket No.: 056937-0109



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

UEMINAMI, MASAHIRO, et al.

Application No.: 10/776,237

Filed: February 12, 2004

For: SEMICONDUCTOR APPARATUS

Customer Number: 20277

Confirmation Number: 2630

Group Art Unit: 2829

Examiner: TANG, Minh Nhut

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☐ Also attached:

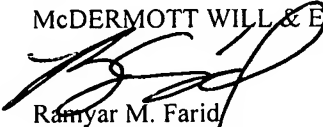
The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	5	20	0	\$50.00 =	\$0.00
Independent Claims	1	9	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Ramyar M. Farid  
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Date: October 26, 2005

**Please recognize our Customer No. 20277 as our  
correspondence address.**

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For: SEMICONDUCTOR APPARATUS	:	

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 27, 2005, having a three-month shortened statutory period for response set to expire on October 27, 2005, reconsideration of the above-identified application is respectfully requested in view of the following amendment and remarks.

**AMENDMENT TO THE TITLE**

Please replace the existing title with the following new title: A Semiconductor Apparatus Including a Switch Element and Resistance Element Connected in Series.